

TIME TABLE FOR 5TH SEM. ELECTRONICS & TELECOMMUNICATION ENGG.

TIME→ DAYS↓	9:45AM- 10.00AM	10.00AM - 10.50AM	10.50AM- 11.40AM	11-40AM- 12:30PM	12:30PM- 01.20PM	1.20PM- 03:50PM	03:50PM- 4.00PM
MON	THOUGHT OF THE DAY & STATE SONG	E &M & SMART TECHNOLOGY	P E & PLC	VLSI & E SYSTEM	B R E A K	VLSI & E SYSTEM LAB	NATIONAL SONG
TUE		E &M & SMART TECHNOLOGY	A D C	VLSI & E SYSTEM		A D C LAB	
WED		Students Centred Activity				P E & PLC LAB.	
THU		E &M & SMART TECHNOLOGY	W P C E	A D C		P E & PLC LAB.	
FRI		VLSI & E SYSTEM	W P C E	P E & PLC		W P C E LAB	
SAT		P E & PLC	W P C E	A D C		PROJECT PHASE-I	

THEORY: -

TH-1- E &M & SMART TECHNOLOGY— ER. K. Maharna
 TH-2- VLSI & E SYSTEM —ER. Jagatjit Sahoo
 TH-3- ANALOG & DIGITAL COMMUNICATION ---Er. M . Nayak
 TH-4- W P C E - - Er. G. Dutta
 TH-5- P E & PLC - Er. S. N . Dey

PRACTICAL:

PR-1- - A D C LAB -- Er. M . Nayak
 PR-2- - VLSI & E SYSTEM LAB — Er. J. Sahoo
 PR-3- - W P C E LAB- -Er. G. Dutta
 PR- 4- - POWER ELECTRONICS LAB - -Er. S. N. Dey
 PR- 5- - PROJECT PHASE -1 - - ER. M. NAYAK